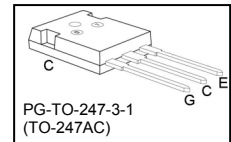
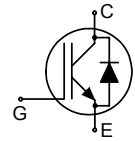


## Fast IGBT in NPT-technology with soft, fast recovery anti-parallel EmCon diode

- 75% lower  $E_{off}$  compared to previous generation combined with low conduction losses
- Short circuit withstand time – 10  $\mu$ s
- Designed for:
  - Motor controls
  - Inverter
- NPT-Technology for 600V applications offers:
  - very tight parameter distribution
  - high ruggedness, temperature stable behaviour
  - parallel switching capability
- Very soft, fast recovery anti-parallel EmCon diode
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC<sup>1</sup> for target applications
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Type	$V_{CE}$	$I_C$	$V_{CE(sat)}$	$T_j$	Marking	Package
SKW20N60	600V	20A	2.4V	150°C	K20N60	PG-TO-247-3-21

### Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{CE}$	600	V
DC collector current	$I_C$	40	A
$T_C = 25^\circ\text{C}$		40	
$T_C = 100^\circ\text{C}$		20	
Pulsed collector current, $t_p$ limited by $T_{jmax}$	$I_{Cpuls}$	80	
Turn off safe operating area	-	80	
$V_{CE} \leq 600\text{V}, T_j \leq 150^\circ\text{C}$			
Diode forward current	$I_F$	40	
$T_C = 25^\circ\text{C}$		40	
$T_C = 100^\circ\text{C}$		20	
Diode pulsed current, $t_p$ limited by $T_{jmax}$	$I_{Fpuls}$	80	
Gate-emitter voltage	$V_{GE}$	$\pm 20$	V
Short circuit withstand time <sup>2</sup>	$t_{SC}$	10	$\mu$ s
$V_{GE} = 15\text{V}, V_{CC} \leq 600\text{V}, T_j \leq 150^\circ\text{C}$			
Power dissipation	$P_{tot}$	179	W
$T_C = 25^\circ\text{C}$			
Soldering temperature	$T_s$	260	$^\circ\text{C}$
wavesoldering, 1.6 mm (0.063 in.) from case for 10s			
Operating junction and storage temperature	$T_j, T_{stg}$	-55...+150	$^\circ\text{C}$

<sup>1</sup> J-STD-020 and JESD-022

<sup>2</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

**Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value	Unit
<b>Characteristic</b>				
IGBT thermal resistance, junction – case	$R_{thJC}$		0.7	K/W
Diode thermal resistance, junction – case	$R_{thJCD}$		1.3	
Thermal resistance, junction – ambient	$R_{thJA}$		40	

**Electrical Characteristic, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0V, I_C=500\mu A$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE} = 15V, I_C=20A$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.7 -	2 2.4	2.4 2.9	
Diode forward voltage	$V_F$	$V_{GE}=0V, I_F=20A$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.2 -	1.4 1.25	1.8 1.65	
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C=700\mu A, V_{CE}=V_{GE}$	3	4	5	
Zero gate voltage collector current	$I_{CES}$	$V_{CE}=600V, V_{GE}=0V$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	- -	- -	40 2500	$\mu A$
Gate-emitter leakage current	$I_{GES}$	$V_{CE}=0V, V_{GE}=20V$	-	-	100	
Transconductance	$g_{fs}$	$V_{CE}=20V, I_C=20A$	-	14	-	S
<b>Dynamic Characteristic</b>						
Input capacitance	$C_{iss}$	$V_{CE}=25V,$ $V_{GE}=0V,$ $f=1\text{MHz}$	-	1100	1320	pF
Output capacitance	$C_{oss}$		-	107	128	
Reverse transfer capacitance	$C_{rss}$		-	63	76	
Gate charge	$Q_{Gate}$	$V_{CC}=480V, I_C=20A$ $V_{GE}=15V$	-	100	130	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	13	-	nH
Short circuit collector current <sup>1)</sup>	$I_{C(SC)}$	$V_{GE}=15V, t_{SC}\leq 10\mu s$ $V_{CC}\leq 600V,$ $T_j\leq 150^\circ\text{C}$	-	200	-	A

<sup>1)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

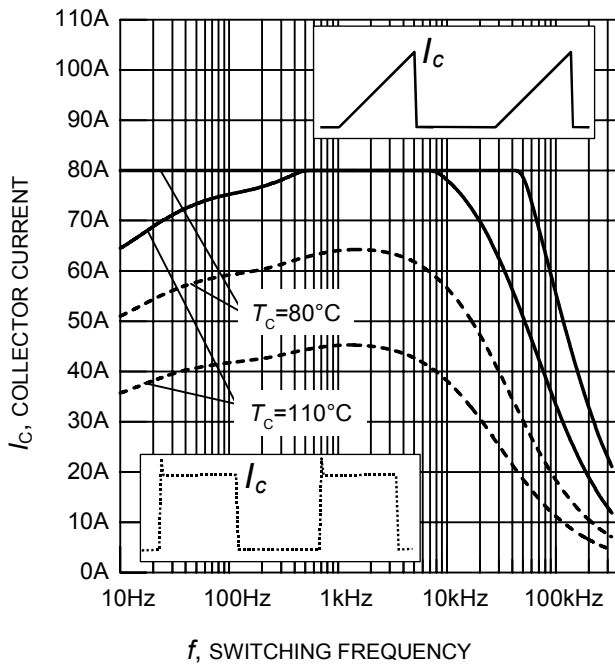
**Switching Characteristic, Inductive Load, at  $T_j=25\text{ }^\circ\text{C}$** 

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-on delay time	$t_{d(on)}$	$T_j=25\text{ }^\circ\text{C}$ , $V_{CC}=400\text{V}$ , $I_C=20\text{A}$ , $V_{GE}=0/15\text{V}$ , $R_G=16\Omega$ , $L_{\sigma}^{(1)}=180\text{nH}$ , $C_{\sigma}^{(1)}=900\text{pF}$ Energy losses include "tail" and diode reverse recovery.	-	36	46	ns
Rise time	$t_r$		-	30	36	
Turn-off delay time	$t_{d(off)}$		-	225	270	
Fall time	$t_f$		-	54	65	
Turn-on energy	$E_{on}$		-	0.44	0.53	mJ
Turn-off energy	$E_{off}$		-	0.33	0.43	
Total switching energy	$E_{ts}$		-	0.77	0.96	
<b>Anti-Parallel Diode Characteristic</b>						
Diode reverse recovery time	$t_{rr}$	$T_j=25\text{ }^\circ\text{C}$ , $V_R=200\text{V}$ , $I_F=20\text{A}$ , $di_F/dt=200\text{A}/\mu\text{s}$	-	300	-	ns
	$t_S$		-	30	-	
	$t_F$		-	270	-	
Diode reverse recovery charge	$Q_{rr}$		-	490	-	nC
Diode peak reverse recovery current	$I_{rrm}$		-	5.5	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	180	-	A/ $\mu\text{s}$

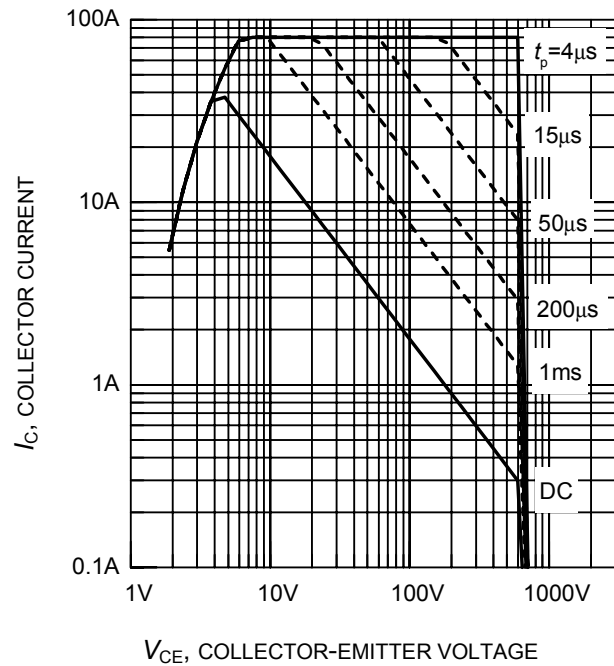
**Switching Characteristic, Inductive Load, at  $T_j=150\text{ }^\circ\text{C}$** 

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-on delay time	$t_{d(on)}$	$T_j=150\text{ }^\circ\text{C}$ $V_{CC}=400\text{V}$ , $I_C=20\text{A}$ , $V_{GE}=0/15\text{V}$ , $R_G=16\Omega$ , $L_{\sigma}^{(1)}=180\text{nH}$ , $C_{\sigma}^{(1)}=900\text{pF}$ Energy losses include "tail" and diode reverse recovery.	-	36	46	ns
Rise time	$t_r$		-	30	36	
Turn-off delay time	$t_{d(off)}$		-	250	300	
Fall time	$t_f$		-	63	76	
Turn-on energy	$E_{on}$		-	0.67	0.81	mJ
Turn-off energy	$E_{off}$		-	0.49	0.64	
Total switching energy	$E_{ts}$		-	1.12	1.45	
<b>Anti-Parallel Diode Characteristic</b>						
Diode reverse recovery time	$t_{rr}$	$T_j=150\text{ }^\circ\text{C}$ $V_R=200\text{V}$ , $I_F=20\text{A}$ , $di_F/dt=200\text{A}/\mu\text{s}$	-	410	-	ns
	$t_S$		-	45	-	
	$t_F$		-	365	-	
Diode reverse recovery charge	$Q_{rr}$		-	1270	-	nC
Diode peak reverse recovery current	$I_{rrm}$		-	8.5	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	200	-	A/ $\mu\text{s}$

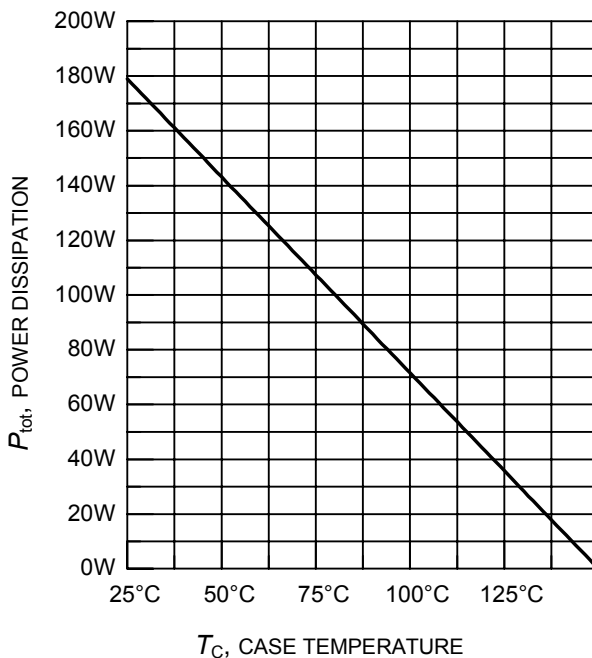
<sup>1)</sup> Leakage inductance  $L_{\sigma}$  and Stray capacity  $C_{\sigma}$  due to dynamic test circuit in Figure E.



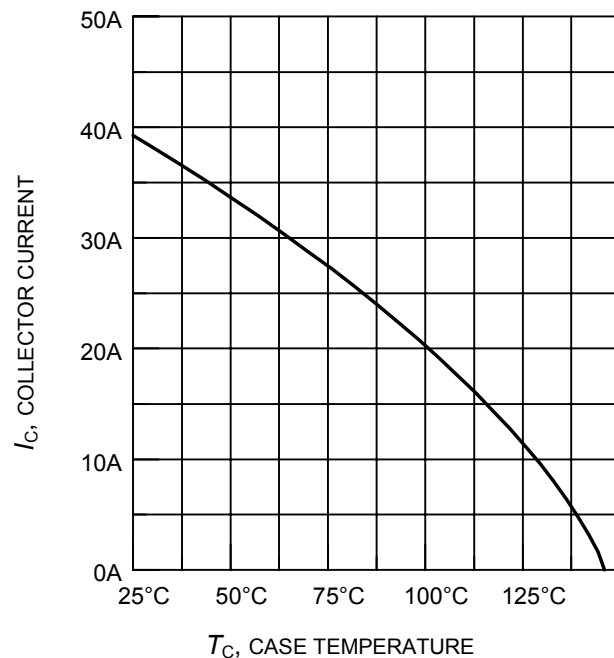
**Figure 1. Collector current as a function of switching frequency**  
 ( $T_j \leq 150^\circ\text{C}$ ,  $D = 0.5$ ,  $V_{CE} = 400\text{V}$ ,  
 $V_{GE} = 0/+15\text{V}$ ,  $R_G = 16\Omega$ )



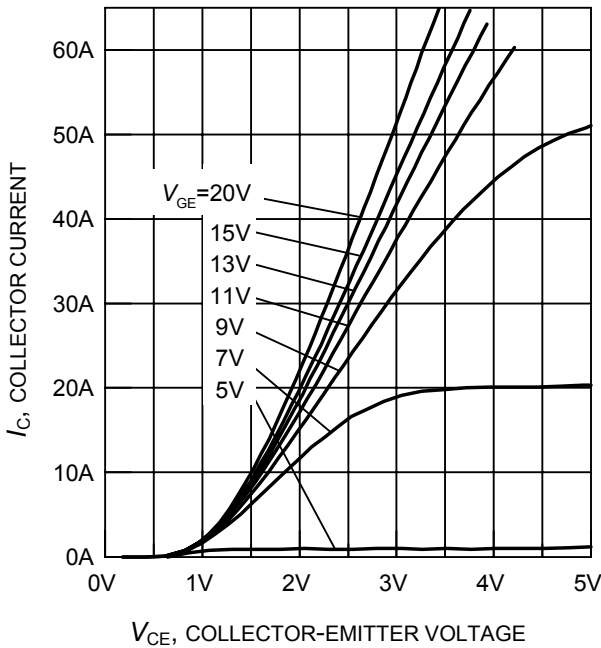
**Figure 2. Safe operating area**  
 ( $D = 0$ ,  $T_c = 25^\circ\text{C}$ ,  $T_j \leq 150^\circ\text{C}$ )



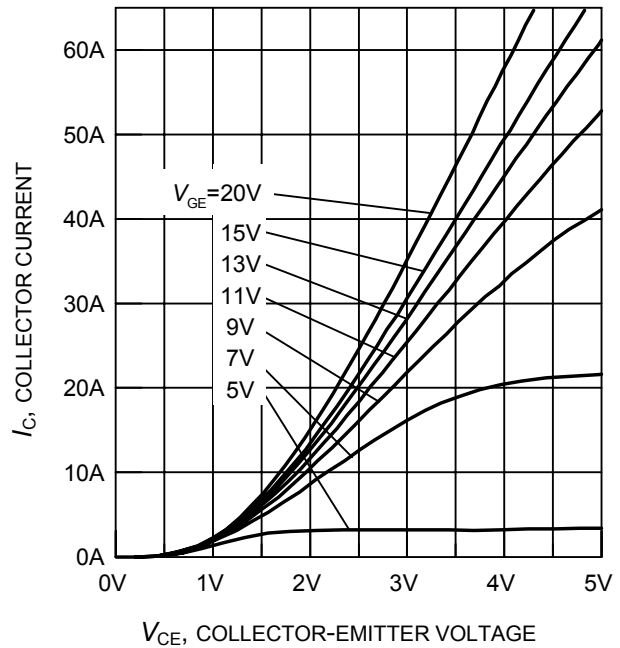
**Figure 3. Power dissipation as a function of case temperature**  
 ( $T_j \leq 150^\circ\text{C}$ )



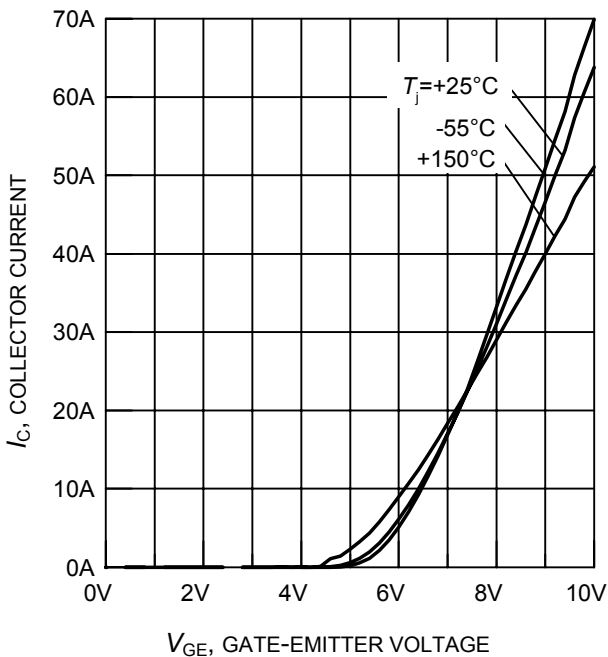
**Figure 4. Collector current as a function of case temperature**  
 ( $V_{GE} \leq 15\text{V}$ ,  $T_j \leq 150^\circ\text{C}$ )



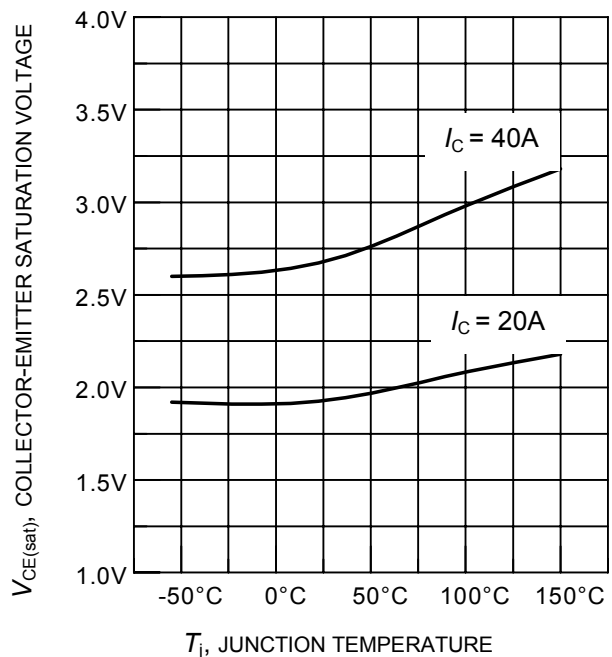
**Figure 5. Typical output characteristics**  
( $T_j = 25^\circ\text{C}$ )



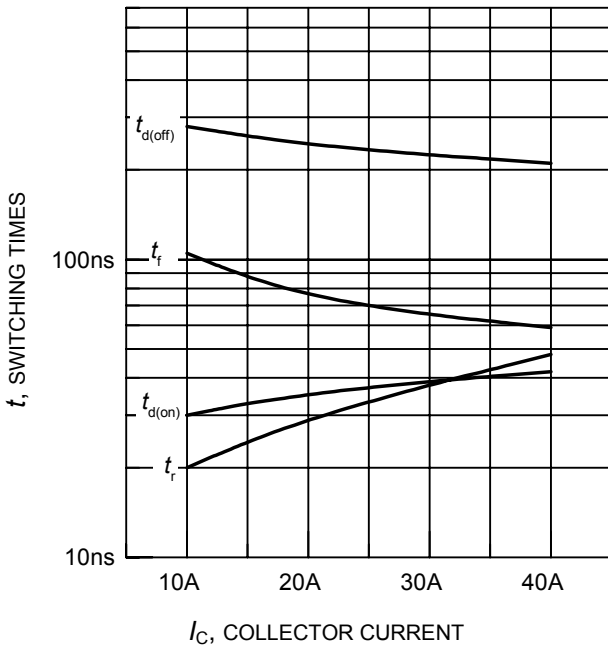
**Figure 6. Typical output characteristics**  
( $T_j = 150^\circ\text{C}$ )



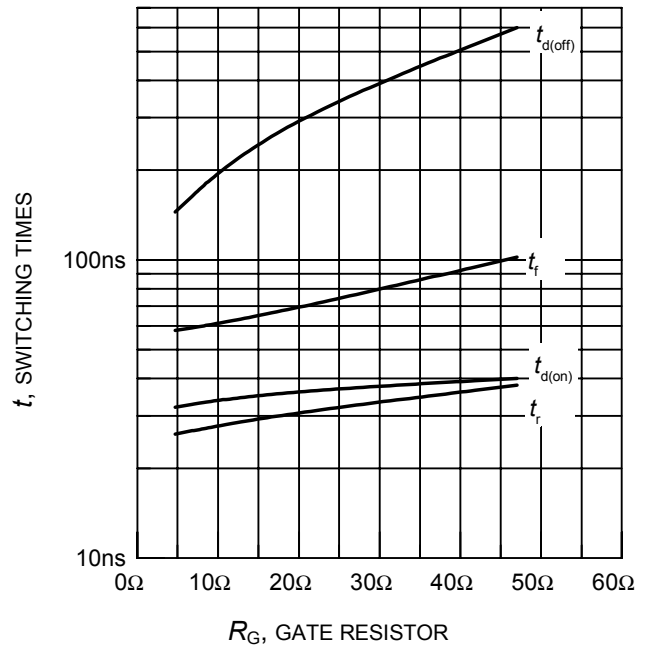
**Figure 7. Typical transfer characteristics**  
( $V_{CE} = 10\text{V}$ )



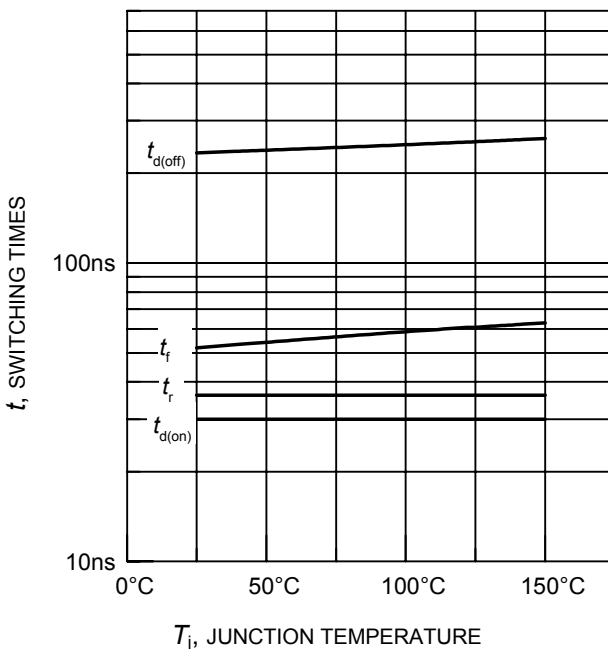
**Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature**  
( $V_{GE} = 15\text{V}$ )



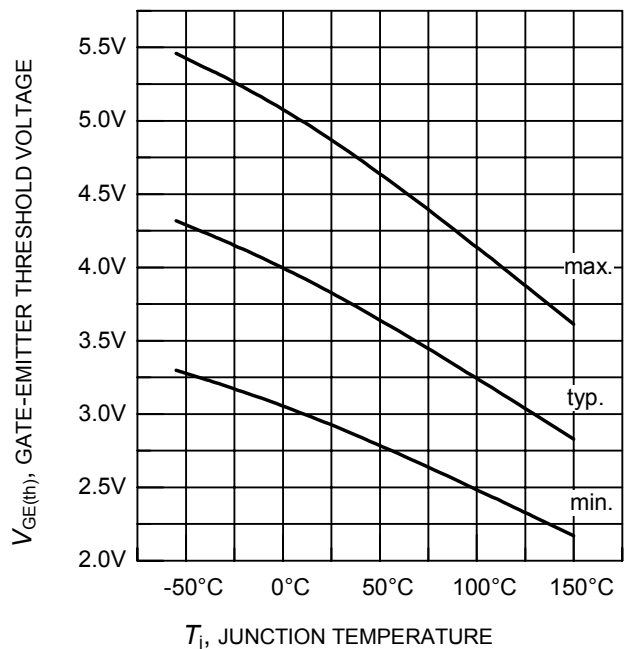
**Figure 9. Typical switching times as a function of collector current**  
 (inductive load,  $T_j = 150^\circ\text{C}$ ,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $R_G = 16\Omega$ ,  
 Dynamic test circuit in Figure E)



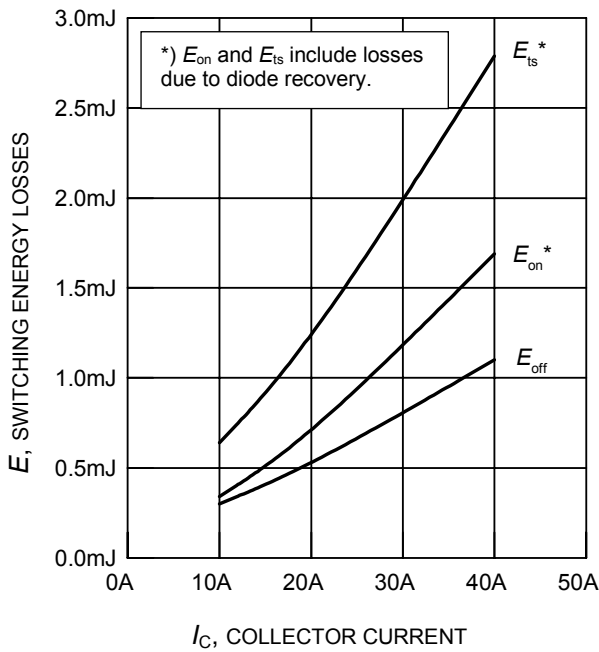
**Figure 10. Typical switching times as a function of gate resistor**  
 (inductive load,  $T_j = 150^\circ\text{C}$ ,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $I_C = 20\text{A}$ ,  
 Dynamic test circuit in Figure E)



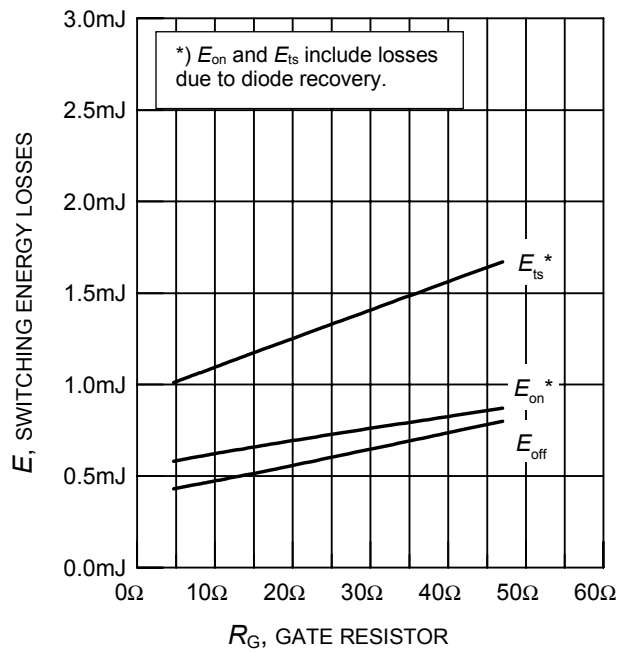
**Figure 11. Typical switching times as a function of junction temperature**  
 (inductive load,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $I_C = 20\text{A}$ ,  $R_G = 16\Omega$ ,  
 Dynamic test circuit in Figure E)



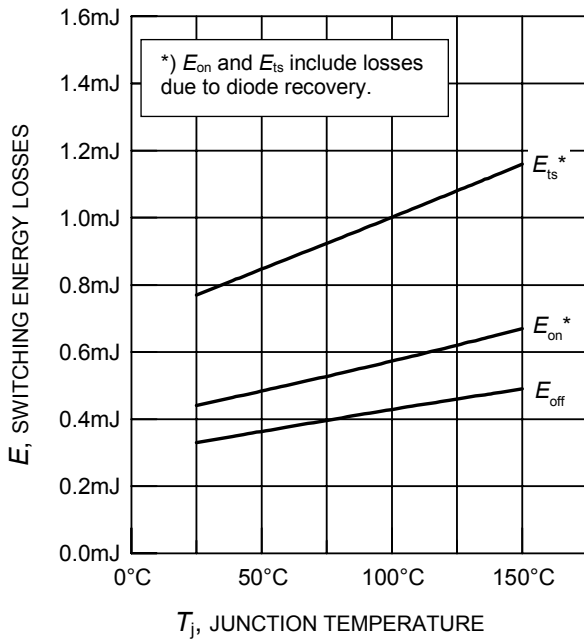
**Figure 12. Gate-emitter threshold voltage as a function of junction temperature**  
 ( $I_C = 0.7\text{mA}$ )



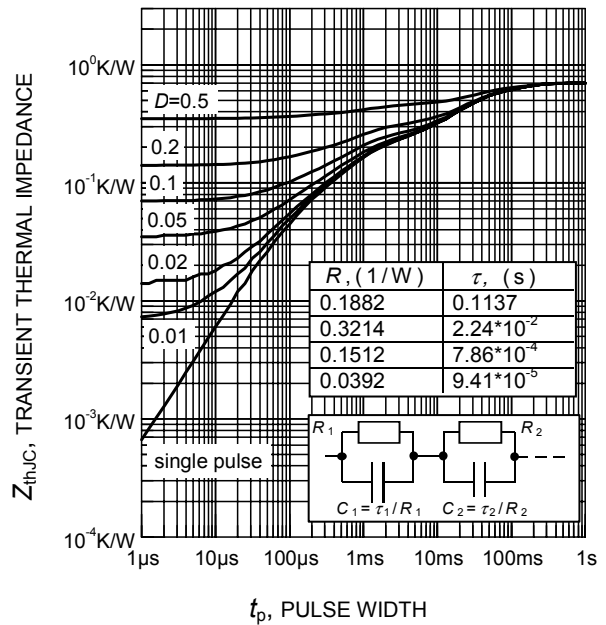
**Figure 13. Typical switching energy losses as a function of collector current**  
 (inductive load,  $T_j = 150^\circ\text{C}$ ,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $R_G = 16\Omega$ ,  
 Dynamic test circuit in Figure E)



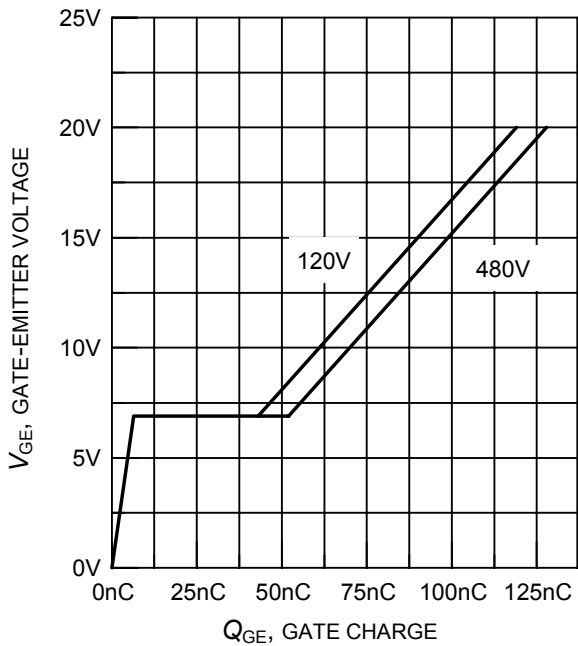
**Figure 14. Typical switching energy losses as a function of gate resistor**  
 (inductive load,  $T_j = 150^\circ\text{C}$ ,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $I_C = 20\text{A}$ ,  
 Dynamic test circuit in Figure E)



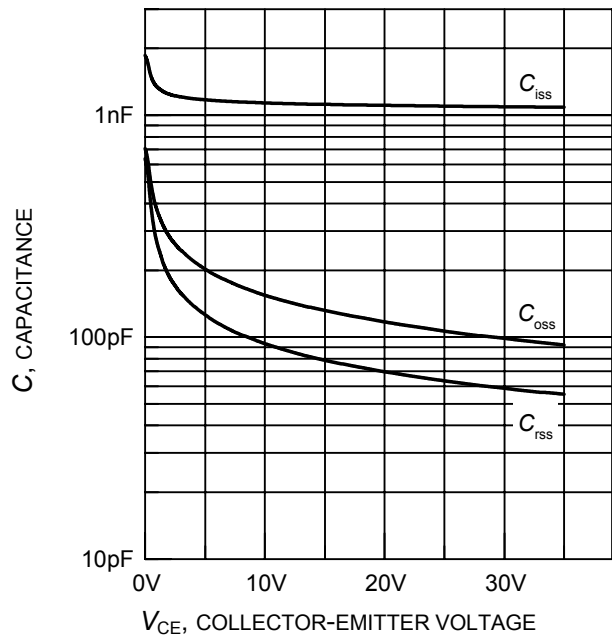
**Figure 15. Typical switching energy losses as a function of junction temperature**  
 (inductive load,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $I_C = 20\text{A}$ ,  $R_G = 16\Omega$ ,  
 Dynamic test circuit in Figure E)



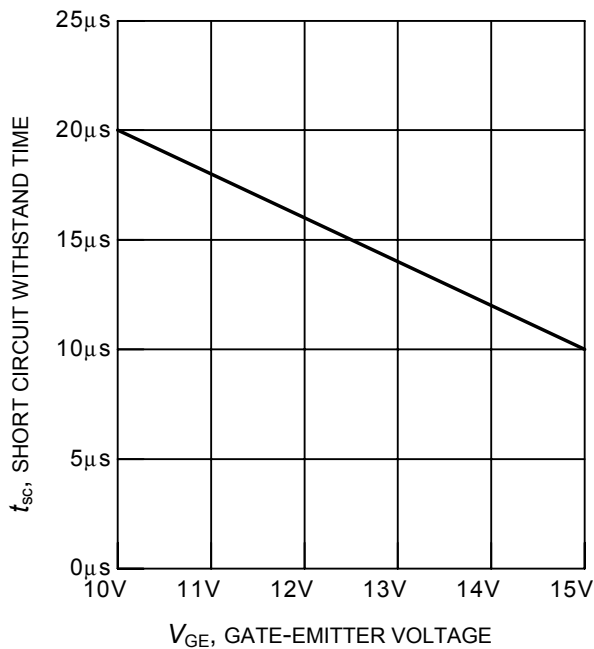
**Figure 16. IGBT transient thermal impedance as a function of pulse width**  
 ( $D = t_p / T$ )



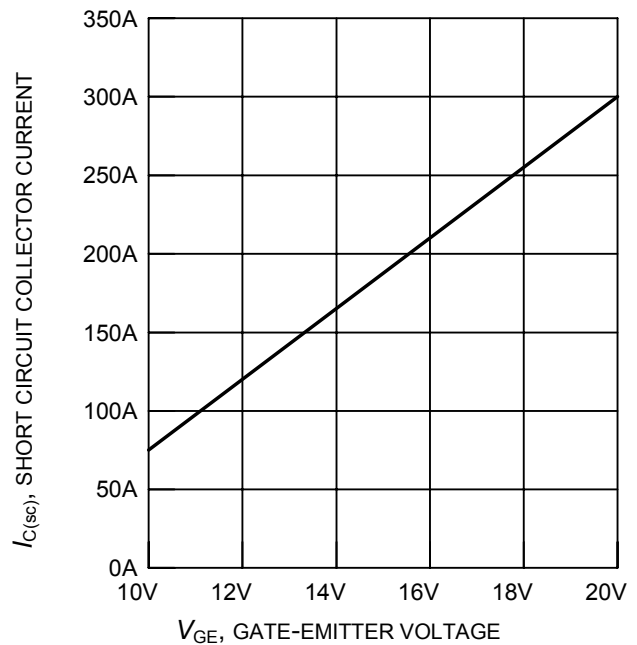
**Figure 17. Typical gate charge**  
( $I_C = 20A$ )



**Figure 18. Typical capacitance as a function of collector-emitter voltage**  
( $V_{GE} = 0V, f = 1MHz$ )

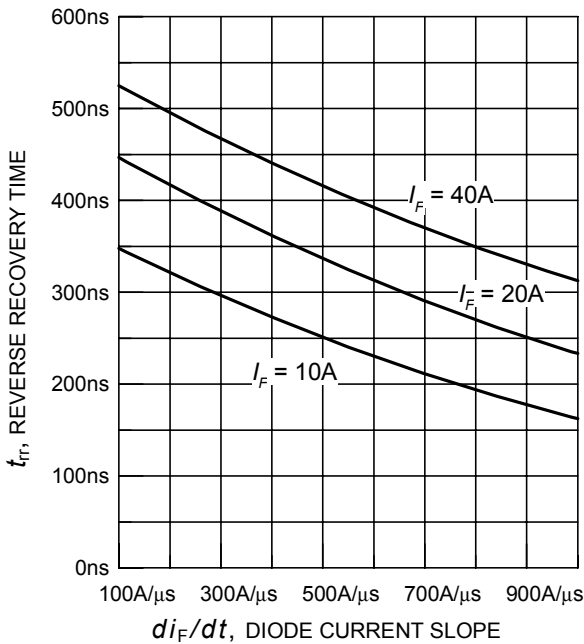


**Figure 19. Short circuit withstand time as a function of gate-emitter voltage**  
( $V_{CE} = 600V$ , start at  $T_j = 25^\circ C$ )

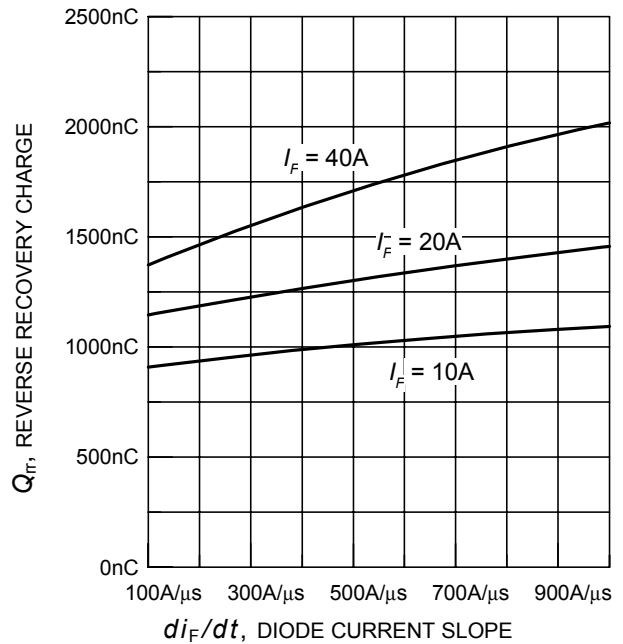


**Figure 20. Typical short circuit collector current as a function of gate-emitter voltage**  
( $V_{CE} \leq 600V, T_j = 150^\circ C$ )

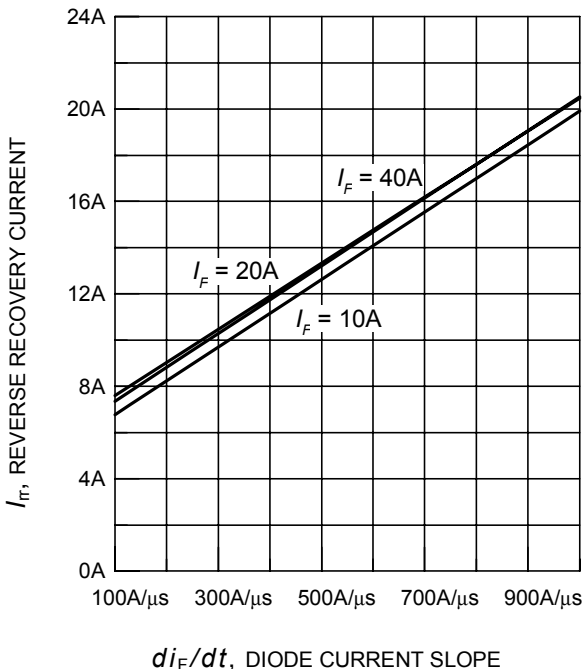




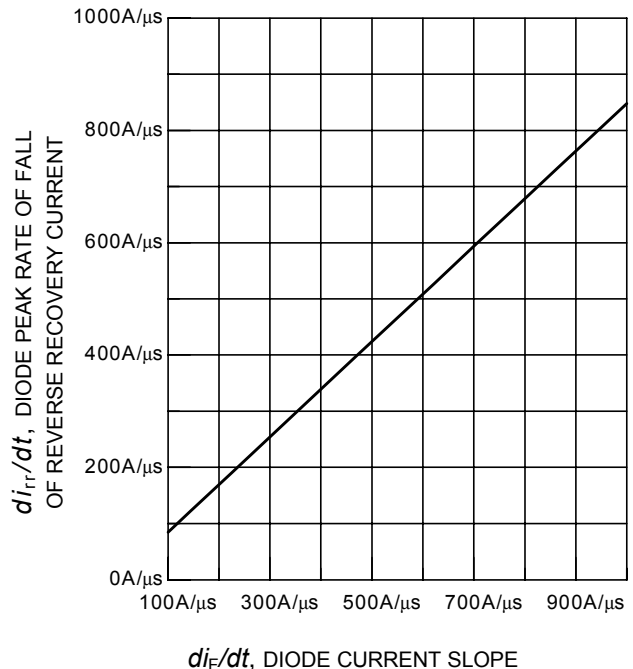
**Figure 21. Typical reverse recovery time as a function of diode current slope**  
 ( $V_R = 200V$ ,  $T_j = 125^\circ C$ ,  
 Dynamic test circuit in Figure E)



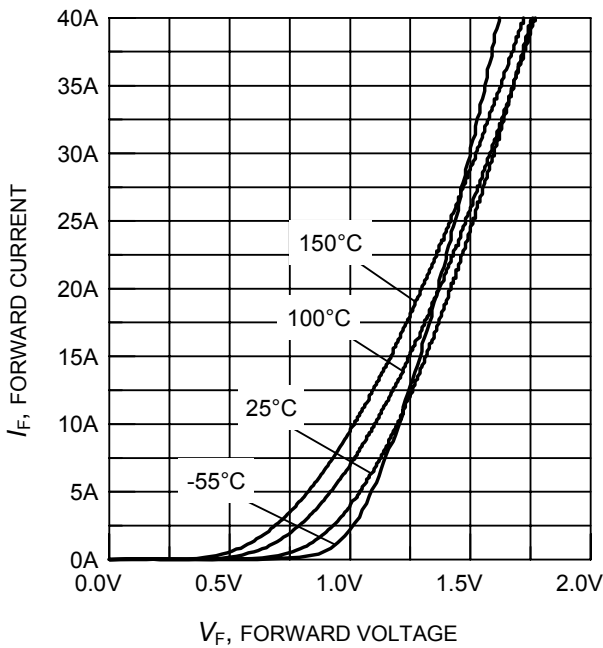
**Figure 22. Typical reverse recovery charge as a function of diode current slope**  
 ( $V_R = 200V$ ,  $T_j = 125^\circ C$ ,  
 Dynamic test circuit in Figure E)



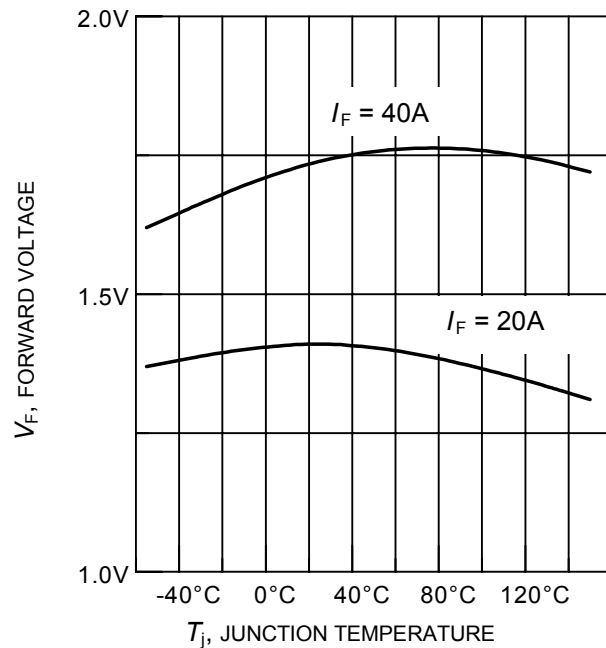
**Figure 23. Typical reverse recovery current as a function of diode current slope**  
 ( $V_R = 200V$ ,  $T_j = 125^\circ C$ ,  
 Dynamic test circuit in Figure E)



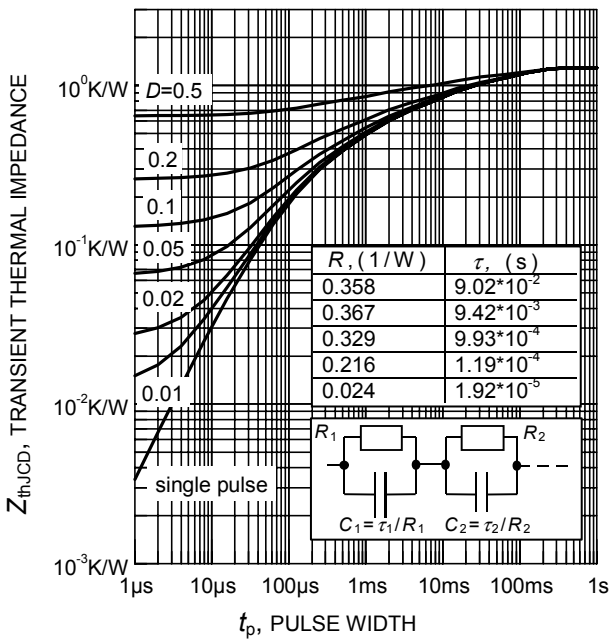
**Figure 24. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope**  
 ( $V_R = 200V$ ,  $T_j = 125^\circ C$ ,  
 Dynamic test circuit in Figure E)



**Figure 25. Typical diode forward current as a function of forward voltage**

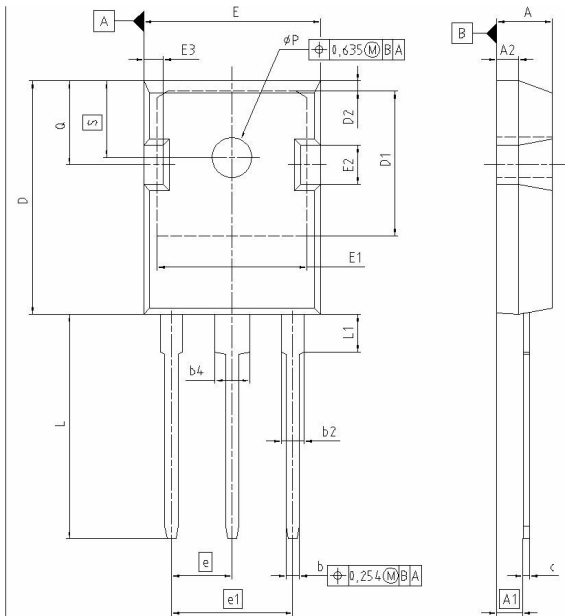


**Figure 26. Typical diode forward voltage as a function of junction temperature**

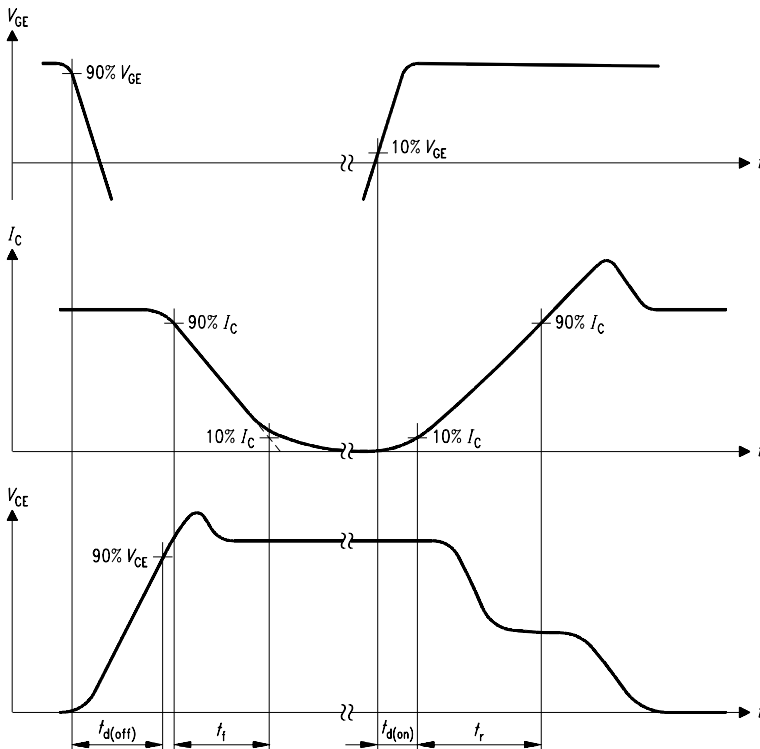


**Figure 27. Diode transient thermal impedance as a function of pulse width ( $D = t_p / T$ )**

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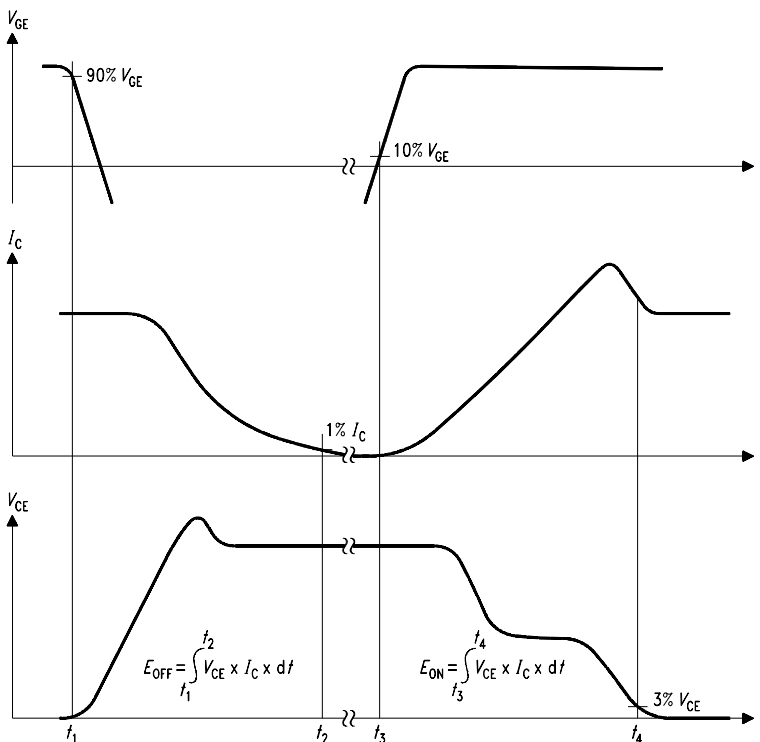


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.903	5.157	0.193	0.203
A1	2.273	2.527	0.092	0.096
A2	1.853	2.107	0.075	0.081
b	1.073	1.327	0.047	0.052
b2	1.903	2.386	0.075	0.094
b4	2.870	3.454	0.113	0.136
c	0.549	0.752	0.024	0.030
D	20.823	21.077	0.820	0.830
D1	17.323	17.831	0.682	0.702
D2	1.063	1.317	0.042	0.052
E	15.773	16.027	0.621	0.631
E1	13.893	14.147	0.547	0.557
E2	3.683	3.937	0.145	0.155
E3	1.683	1.937	0.066	0.076
e	5.450		0.215	
e1	10.900		0.430	
N	3		3	
L	20.053	20.307	0.789	0.799
L1	4.168	4.472	0.164	0.176
pP	3.559	3.661	0.140	0.144
Q	5.493	5.747	0.216	0.226
S	6.043	6.297	0.238	0.248



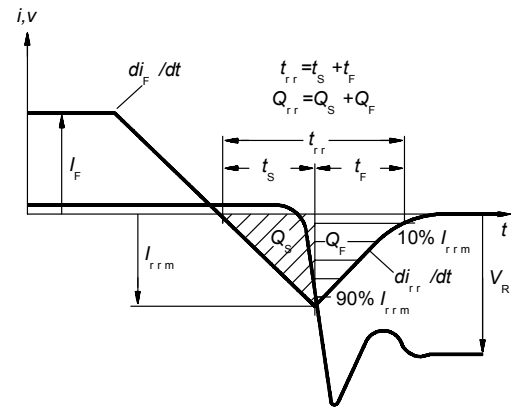
**Figure A. Definition of switching times**

SIS00053

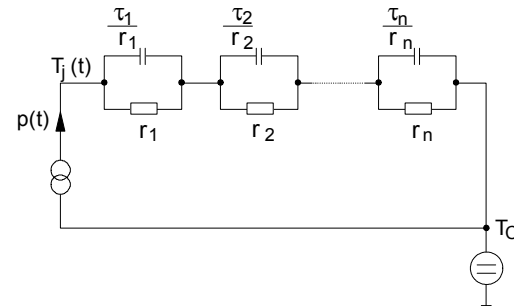


**Figure B. Definition of switching losses**

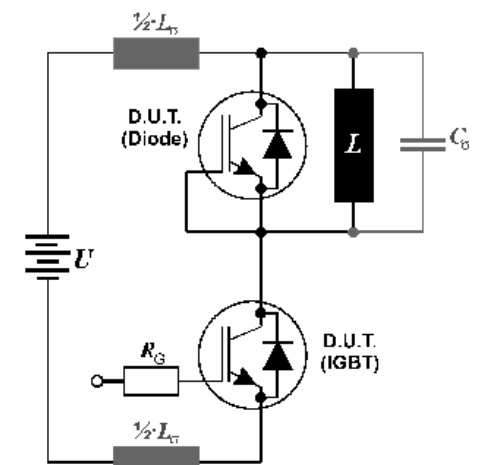
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**Figure C. Definition of diodes switching characteristics**



**Figure D. Thermal equivalent circuit**



**Figure E. Dynamic test circuit**  
Leakage inductance  $L_{\sigma} = 180\text{nH}$   
and Stray capacity  $C_{\sigma} = 900\text{pF}$ .

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